

WEST Search History

Hide Items **Restore** **Clear** **Cancel**

DATE: Saturday, September 04, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L11	L10 and substrate	1
<input type="checkbox"/>	L10	circuit and (check\$ or detect\$) adj6 (error\$ or fault\$ or defect\$) adj6 (processor or microprocessor or micro-processor or CPU)	286
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L9	L6 and (check\$ or detect\$) adj6 (error\$ or fault\$ or defect\$) adj6 (processor or microprocessor or micro-processor or CPU)	3
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L8	substrate and (check\$ or detect\$) adj4 (error\$ or fault\$ or defect\$) adj4 (processor or microprocessor or micro-processor or CPU)	2
<i>DB=PGPB, USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L7	L6 and (check\$ or detect\$) adj4 (error\$ or fault\$ or defect\$) adj4 (processor or microprocessor or micro-processor or CPU)	4
<input type="checkbox"/>	L6	L5 and (processor or microprocessor or micro-processor or CPU)	936
<input type="checkbox"/>	L5	L4 and (chip\$ or IC\$)	5008
<input type="checkbox"/>	L4	L2 and semiconductor	7606
<input type="checkbox"/>	L3	L2 and semiconductor	0
<input type="checkbox"/>	L2	substrate adj3 wiring	10373
<input type="checkbox"/>	L1	10/370518	3

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER